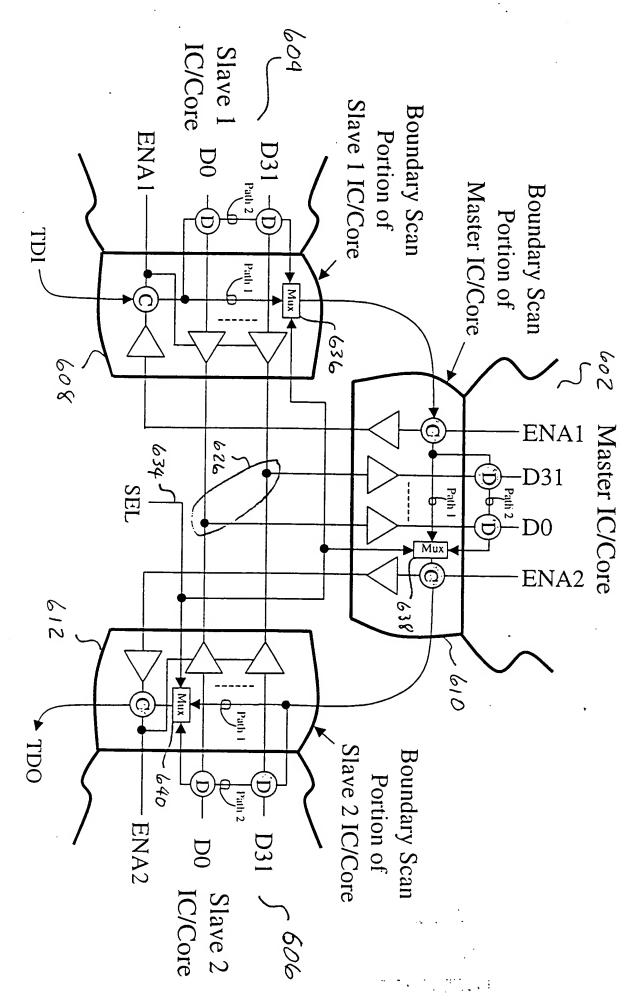


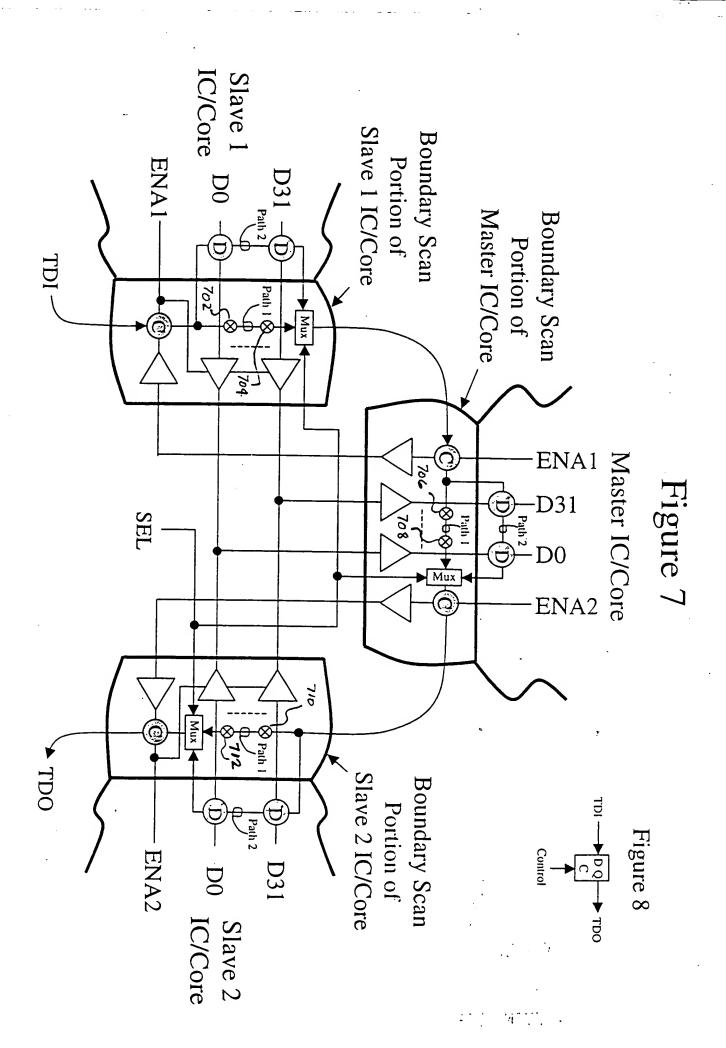
Slave 1 D0 – IC/Core 518~ Boundary Scan
Portion of Slave 1 IC/Core ENA1 D31 -522 Boundary Scan
Portion of (Master IC/Core TDI -528--508 D31 ~ 532 - 510 530 → TDO Portion of Slave 2 IC/Core Boundary Scan - 524 -ENA2 _570 IC/Core D0 Slave 2 506

Figure 5

Master IC/Core

Figure 6





Slave 1 IC/Core D0 -Boundary Scan Portion of Slave 1 IC/Core Boundary Scan Portion of Master IC/Core TDI -2908 808 Master IC/Core Z SEL Mux 912 -216 ➤ TDO Slave 2 IC/Core Boundary Scan -924 Portion of -ENA2 D0 IC/Core Slave 2 906

Figure 9